

PATENT NUMBER

O.I.P.E. SCANNED <i>CLOTS</i> Q.A. <i>401</i>	PATENT DATE
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APPLICATION NO. 09/583233	CONT/PRIOR F	CLASS 365	SUBCLASS 163	ART UNIT 2818 2188	EXAMINER M. Kim
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Semiconductor memory device and method of controlling same

PTO-2040  
12/98[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	<b>Sheets Drwg.</b>	<b>Figs. Drwg.</b>	<b>Print Fig.</b>	<b>Total Claims</b>	<b>Print Claim for O.G.</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____	_____ (Primary Examiner) _____ (Date)			<b>ISSUE FEE</b>	
				<b>Amount Due</b>	<b>Date Paid</b>
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			<b>ISSUE BATCH NUMBER</b>	
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